

IN THE SPECIFICATION:

Please replace the paragraph on page 14, lines 8-28, with the following rewritten paragraph:

C1
X

With reference now to **Figure 4B**, a flowchart illustrates the steps in performing the serial port test. Using the PCI bus, which in this example is assumed to be functional, diagnostic adapter card **300 222** or system processor **202** transfers test data to the serial 1 circuit (step **408**). This data transfer is via PCI bridge **208** and ISA bridge **214**. The data is stored temporarily in the serial 1 buffer (step **410**). Using the serial device control register, serial 1 circuit is instructed to output the data on serial 1 port (step **412**). At the same time, serial 2 circuit has been instructed to receive data at serial port 2 (step **414**). Wrap cable **404** is essential to complete this transfer. The received data is placed in serial 2 buffer (step **416**). This data is then transferred back to the diagnostic adapter card via the PCI bus (step **418**). The received data is compared with the original data (step **420**), results of this comparison are logged (step **422**), and appropriate success or failure test notification is posted to the diagnostics card readout, to the NC video display, or via the network connection.